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CDA4205

Homework 2

1. CPU Time = (Instructions/program) \* (Clock Cycles/Instruction) \* (seconds/clock cycle)
   1. Instructions/program – this refers to the number of instructions contained in the program being run
      1. A factor that can impact this variable is if the program is using a lot of instructions to complete what it needs to do.
   2. Clock Cycles/Instruction – this refers to the number of clock cycles needed to complete a single instruction; this is usually a weighted average based on the clock cycles to completion of each instruction multiplied by the frequency of that instruction
      1. A factor that can impact this variable is if the instruction set that is being used is inefficient or if the program uses a majority of instructions that take many cycles to complete
   3. Seconds/Clock Cycle – this refers to the number of seconds it takes for a single clock cycle
      1. This major factor that can impact this is variable is the clock speed of the processor
2. 1. 21/14 = 1.5, Computer A is 1.5 times faster than computer B.
   2. 1. One factor could be the processor speed. If both systems are running off the same instruction set then computer A would have a faster processor than B allowing it to complete more instructions per cycle
      2. Another factor could be the instruction set being used. Depending on the instruction set used on computer B, it could be requiring more cycles to complete each instruction thereby making it complete the entire program slower than computer A.
3. 1. M1, 2\*(.6) + 3\*(.3) + 3\*(.1) = 2.4 CPI
   2. M2, 3\*(.6) + 2\*(.3) + 1\*(.1) = 2.5 CPI
4. 1. M1-MOD CPI = 2\*(.6) + 2\*(.3) + 3\*(.1) = 2.1 CPI, CPU Time = IC\*2.1\*1/(2.0 \*(10^9))
      1. (IC/2\*10^9) / (IC/2\*10^9) \* (2.4/2.1) = 1.143
      2. M1-MOD is 1.143 times faster than M1
   2. M2-MOD CPI = 2\*(.6) + 2\*(.3) + 1\*(.1) = 1.9 CPI, CPU Time = IC\*1.9\*(1/(2.2\*10^9))
      1. (IC/2.2\*10^9) / (IC/2\*10^9) \*2.5/1.9 = 1.316
      2. M2-MOD is 1.316 times faster than M2
5. By running two instructions per cycle of type A, you would be able to cut that in half.
   1. M1-MOD CPI = 2.1, M1-MOD w/Hardware CPI = 1\*(.6) + 2\*(.3) + 3\*(.1) = 1.5 CPI
      1. (IC/2\*10^9)/(IC/2\*10^9) \* (2.1/1.5) = 1.4
      2. M1-MOD w/Hardware is 1.4 times faster than M1-MOD
   2. M1 CPI = 2.4 CPI
      1. (IC/2\*10^9)/(IC/2\*10^9) \* (2.4/1.5) = 1.6 CPI
      2. M1-MOD w/Hardware is 1.6 times faster than M1
6. CPU-A - CPIa = 1.3, Clock Rate = 1.2\*10^9, compiled instructions 10^6  
   CPU-B - CPIb = 2.1, Clock Rate = 2.5\*10^9
   1. (1\*10^6)(1.3)(1/(1.2\*10^9)) = instructions(2.1)(1/(2.5\*^109))
      1. Instructions = 1.289 \* 10^6
   2. Assuming IC = 1\*10^6
      1. CPU Time A = 10^6 \* 1.3 \* (1/(1.2\*10^9)) = 0.00108333s
      2. CPU Time A \* .5 = 0.00541667s
      3. CPU Time Bnew = 10^6 \* CPI\_Bnew \* (1/(2.5\*10^9)) = CPI\_Bnew \* 0.0004
      4. CPU Time Bnew = CPI\_Bnew \* 0.0004 = 0.00541667s
      5. CPI\_Bnew = 1.354
      6. One factor may be the that it could be very data intensive so there is a low cpu time but a higher time overall because of the transfer speeds from memory
      7. Another factor could be that the processor was required to handle other programs or aspects of the system at the same time meaning that it would push the processes of foo.exe aside temporarily to complete them.
7. Assuming these all have the same CPI as it is not otherwise stated, applying Amdahl’s Law would give the following:
   1. If you were to make the Floating point multiplication run 10x faster:
      1. T\_imp = (.5\*T)/10 + (.5\*T) = .55T
      2. Applying the relative performance equation gives T/.55T = 1.818
   2. If you were to make the Floating point Division 4x faster:
      1. T\_imp = (.3T)/4 + (.7\*T) = 0.775T
      2. Applying the relative performance equation gives T/0.775T = 1.290
   3. As such, neither of these options will result in the machine running this program 2x faster than before.